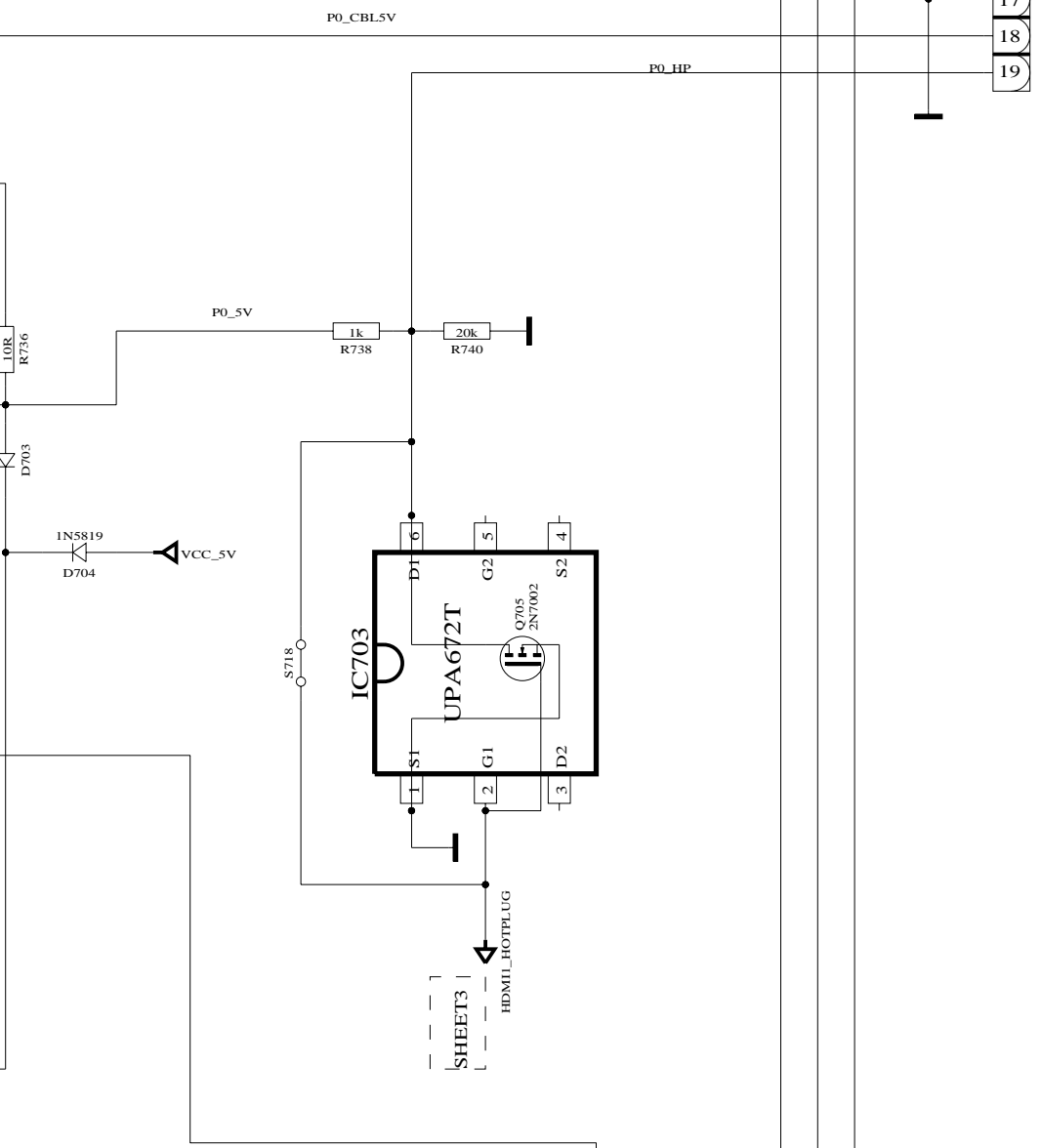
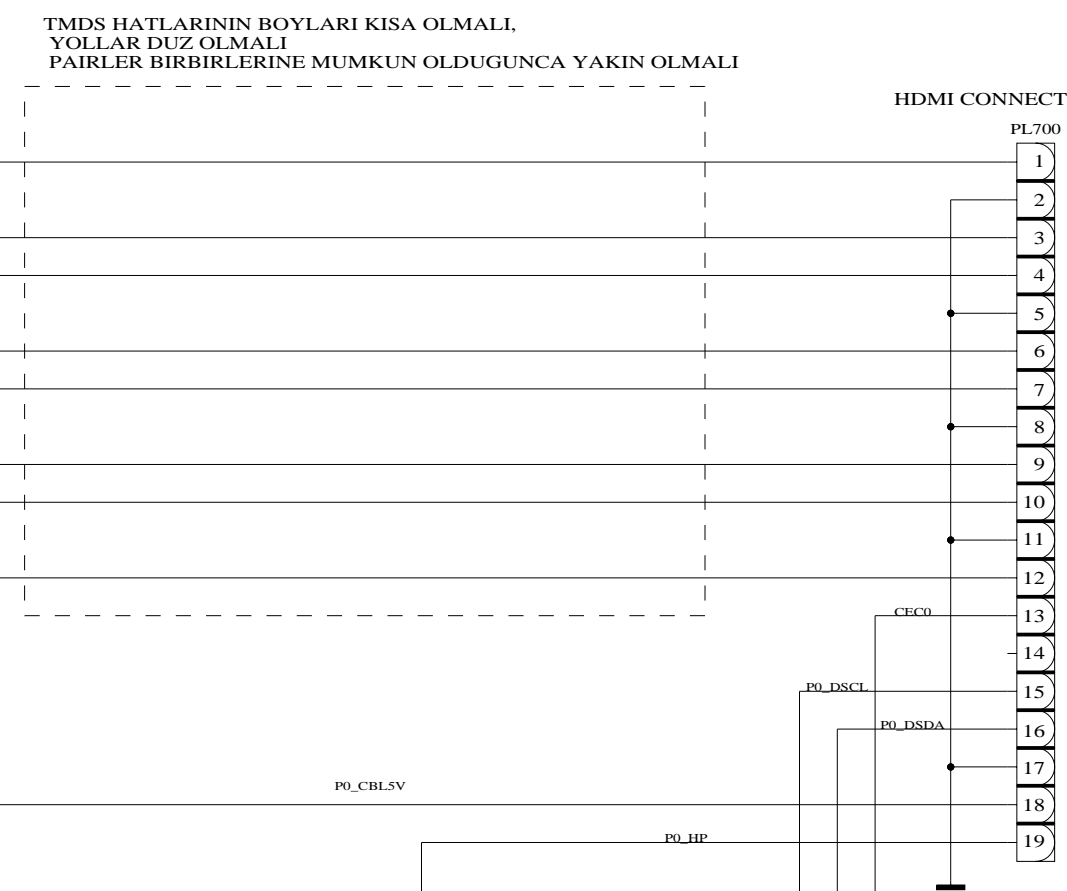
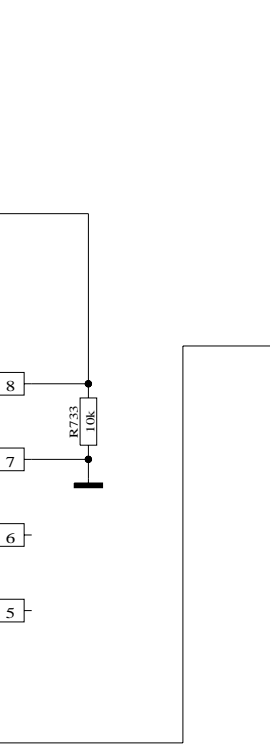
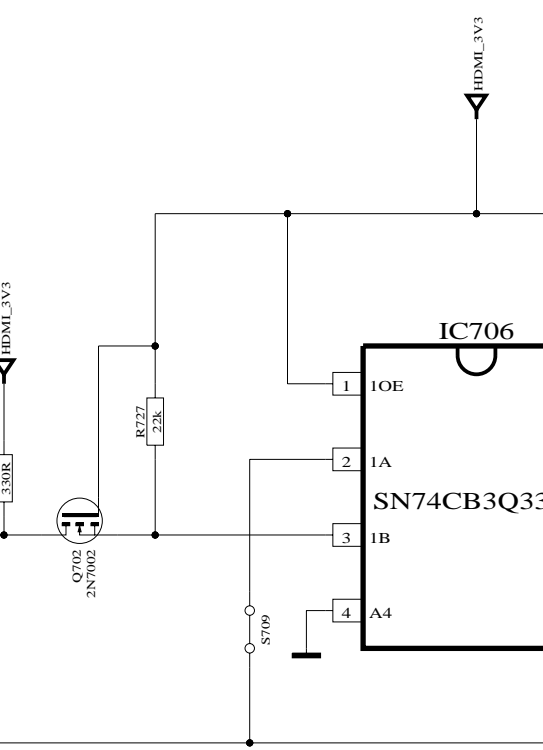
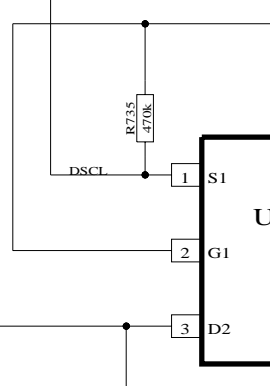
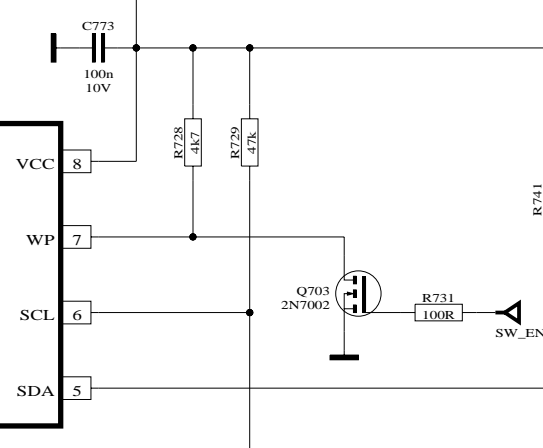
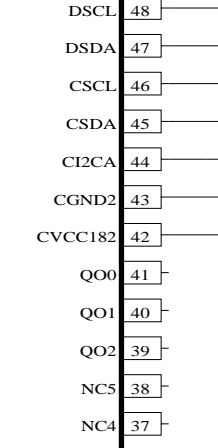
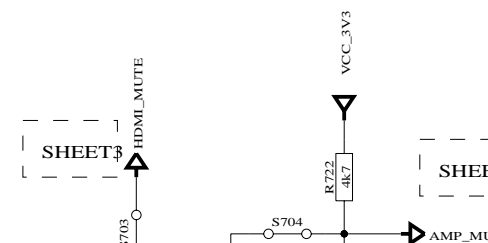
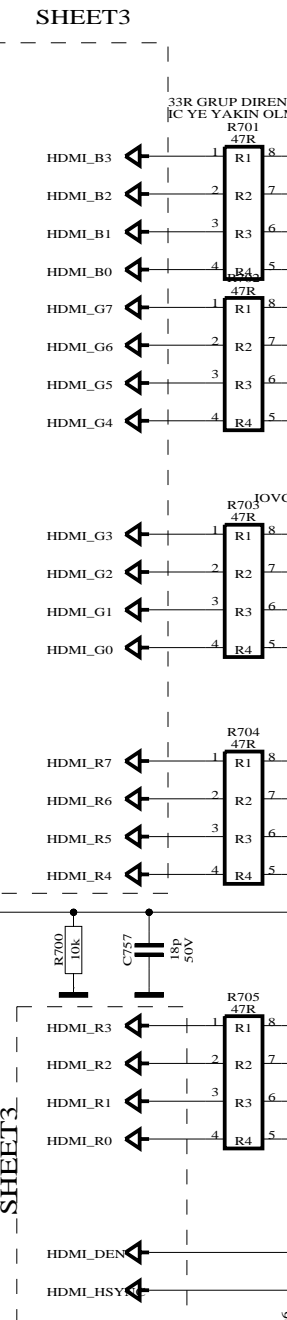


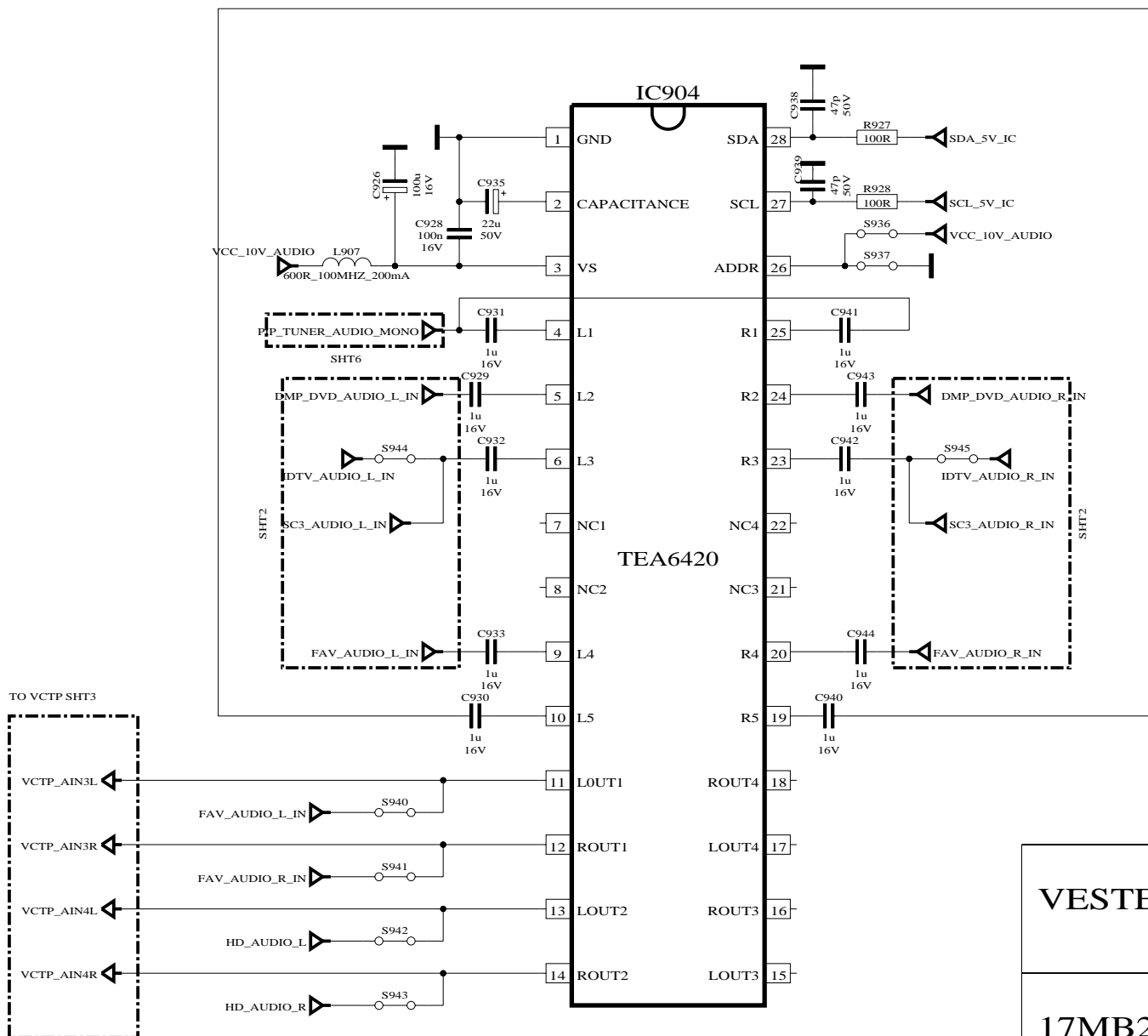
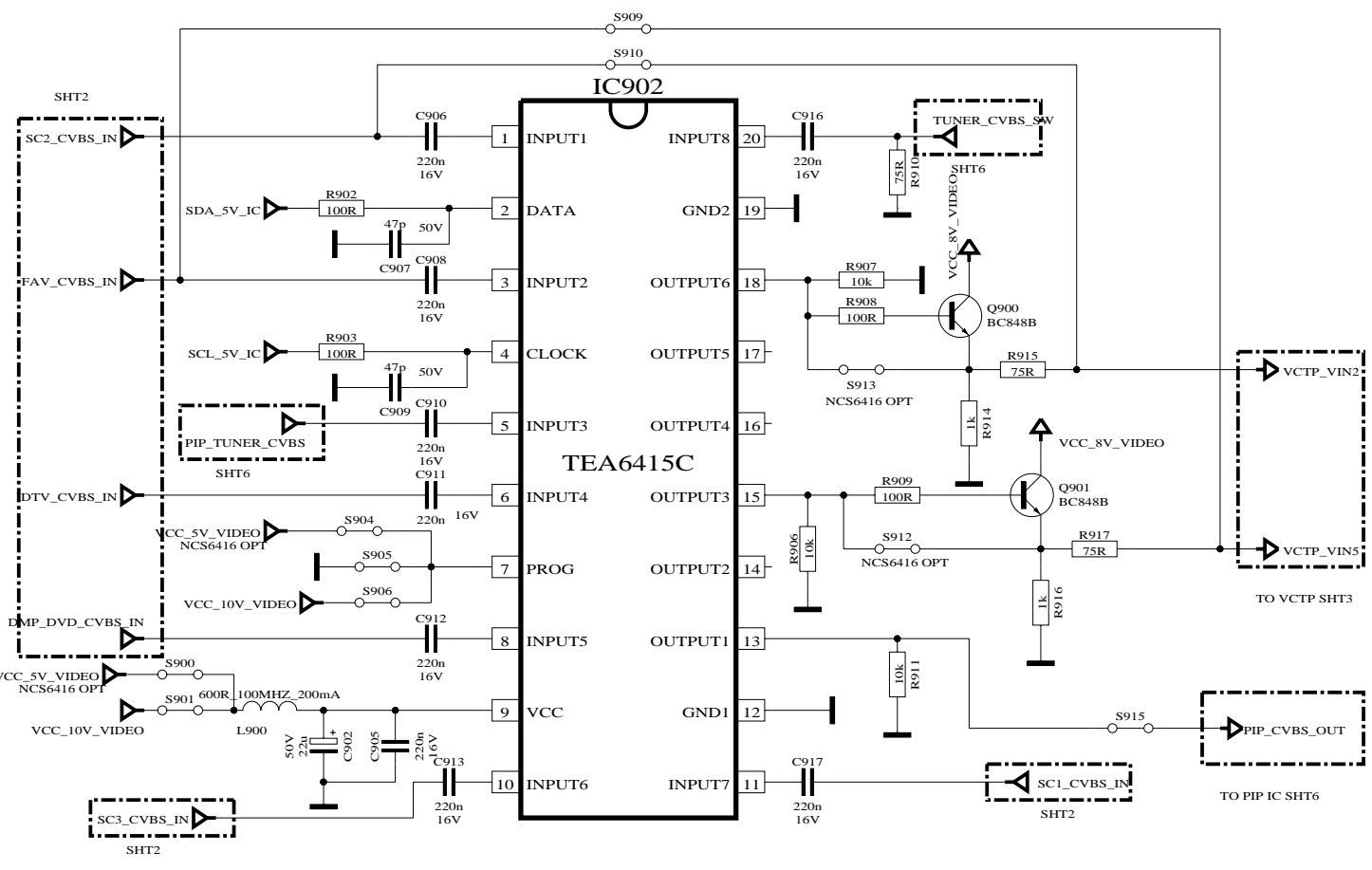
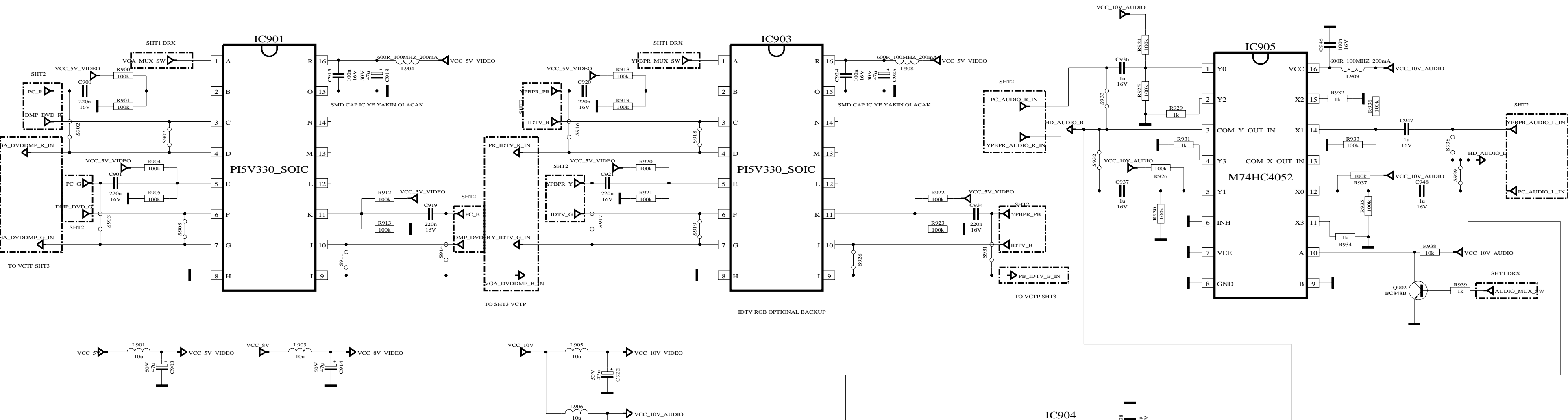


PL701		
	1	80 → HDMM_HOTPLUG
	2	79 → HDMM2_HOTPLUG
HDMM_B0	3	78
HDMM_B1	4	77 → HDMM1_CFC
HDMM_B2	5	76 → HDMM2_CFC
HDMM_B3	6	75
HDMM_B4	7	74 → SCK
HDMM_B5	8	73
HDMM_B6	9	72 → SDO
HDMM_B7	10	71 → WS
	11	70 → HDMM_MUTE
	12	69 → AMP_MUTE
HDMM_G0	13	68
HDMM_G1	14	67 → RESET_IC
HDMM_G2	15	66 → HDMM1_SCDT
HDMM_G3	16	65 → INT
HDMM_G4	17	64
HDMM_G5	18	63
HDMM_G6	19	62 → 1V8_STBY
HDMM_G7	20	61 → 1V8_STBY
	21	60 → 1V8_STBY
	22	59
HDMM_R0	23	58
HDMM_R1	24	57 → 3V3_STBY
HDMM_R2	25	56 → 3V3_STBY
HDMM_R3	26	55 → 3V3_STBY
HDMM_R4	27	54
HDMM_R5	28	53
HDMM_R6	29	52 → 5V_STBY
HDMM_R7	30	51 → 5V_STBY
	31	50 → 5V_STBY
	32	49
HDMM_CL	33	48
	34	47
HDMM_DE	35	46
HDMM_HV	36	45 → SW_ENABLE1
HDMM_VSV	37	44 → SW_ENABLE2
	38	43
SC3_3V3_IC	39	42
	40	41 → SDA_3V3_IC



DECOUPLING VE BYPASS KAPASİTELERİ PINLERE MÜMKÜN OLDUĞUNCA YAKIN OLMALIDIR

VESTEL ELECTRONICS TV R&D GROUP			
17MB22-2	HDMI		
VER. E2	DATE 03/10/2006	MELIH CINAR	SHEET 05



VESTEL ELECTRONICS			
17MB22-2	AUDIO VIDEO SW		
VER E2	DATE	EMRE ARDALI	SHEET 07
	03/10/2006		

